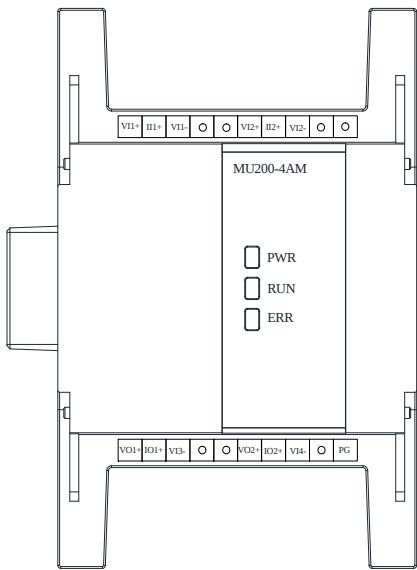
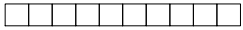


×



ERR	
-----	--

5.2

-10000

10000

5-1

$$D = \frac{S_U - S_L}{U_u - U_L} \times (U - U_L) + S_L \tag{5 - 1}$$

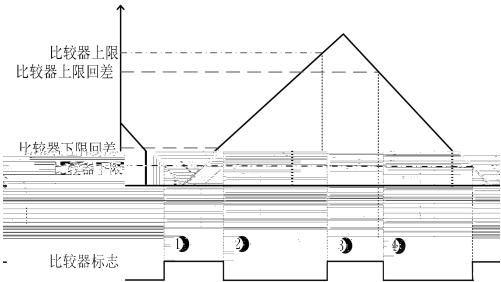
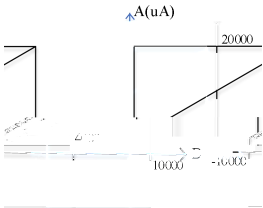
D =

U = ()

U_U = U_L =

S_U = S_L =

1 -



7.

1

2

3.

4

8.

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•

•

1

2

3

4

5

6

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B 5

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518067

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1

6-2

6-2

16Bit	1 0 BIT0: BIT1: BIT8: BIT9: BIT10: BIT11:

2

ID

0

ID

3

6-3

6-3

16Bit	BIT0~3: CH1~4 0 1 BIT4~15:

4

6-4

6-4

16Bit	BIT0~3: CH1~4 0 1 BIT4~15:

5

6-5

6-5

16Bit	BIT0~3: CH1~4 0 1 BIT4~15:

6

6-4

6-4

16Bit	BIT0: CH1 BIT1: CH2 BIT3 BIT15:

7 PLC

0

0~10V

0V

4~20mA

4mA

0V

0mA

8

9

1~100

8

A/D

(D)

A/D